# LINEAR IC

# QUAD OPERATIONAL AMPLIFIER

# **MB3614**

# QUAD OPERATIONAL AMPLIFIER OPERATES FROM A SINGLE OR DUAL POWER SUPPLY

The Fujitsu MB3614 is a Quad operational amplifier having a phase compensatory circuitry and operates from a single power supply or dual power supplies.

The device has equivalent electrical characteristics of current industrial standard operational amplifier and requires low power supply current.

MB3614 can be high density mounted because it integrates 4 circuits in DIP/FPT 14-pin package.

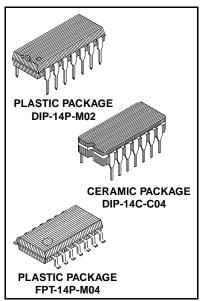
- · No phase compensation required
- · Wide power supply voltage
  - Single power supply: +3 to +30 V
  - Dual power supplies: ±1.5 to ±15 V
- Wide input common mode range: 0 to (Vcc -1.5) V
- Low power supply current: 0.8 mA typ.
- Low input offset voltage: 2 mV typ.
- Package
  - 14-pin Plastic DIP package (Suffix: -P)
  - 14-pin Ceramic DIP package (Suffix: -Z)
  - 14-pin Plastic FPT package (Suffix: -PF)

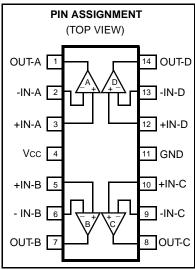
# ■ ABSOLUTE MAXIMUM RATINGS (see NOTE)

 $(T_A=25^{\circ}C)$ 

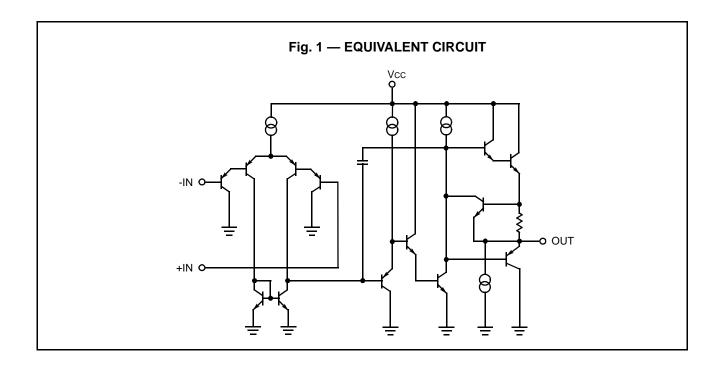
Rating	Symbol	Value	Unit	
Power Supply Voltage		Vcc	36	V
Differential Input Voltage		VID	36	V
Input Common Mode Voltage		Vı	-0.3 to +36	V
Power Dissipation	PD	570	mW	
Operating Temperature		TA	-20 to +75	°C
Storage Temperature	Plastic	Тѕтс	-55 to +125	°C
	Ceramic	Тѕтс	-65 to +150	°C

NOTE: Permanent device damage may occur if the above Absolute Maximum Ratings are exceeded. Functional operation should be restricted to the conditions as detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.



# **■ ELECTRICAL CHARACTERISTICS**

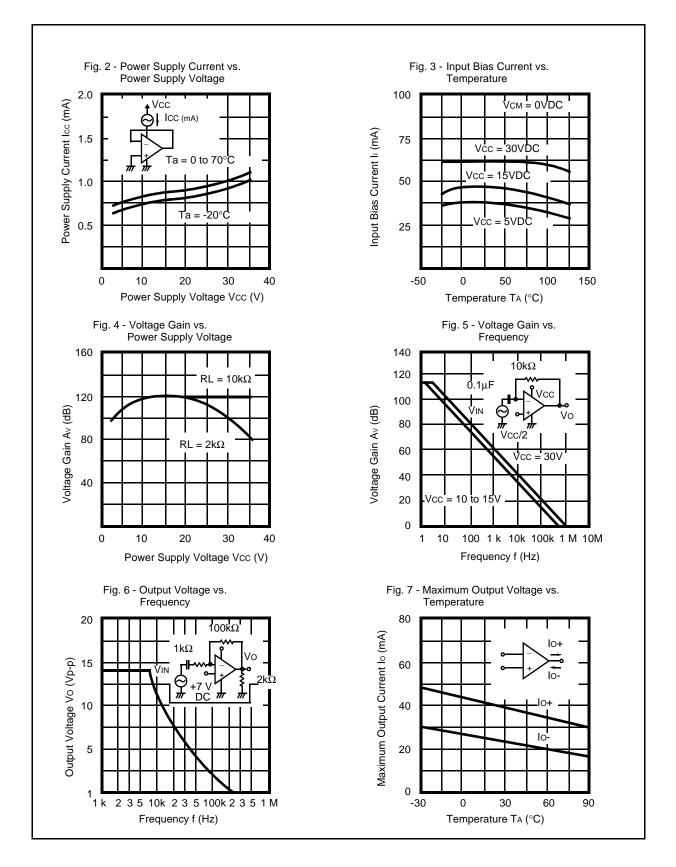
 $(VCC = 5 \text{ V}, TA = 25^{\circ}C)$ 

Parameter	Symbol	Condition	Value			l lmi4
		Condition	Min	Тур	Max	Unit
Input Offset Voltage	Vio	_	_	2	7	mV
Input Offset Current	lio	_	_	5	50	nA
Input Bias Current	<b>l</b> ı*	_	_	45	250	nA
Power Supply Current	Icc	R <sub>L</sub> = ∞	_	0.8	2.0	mA
Input Common Mode Voltage	Vсм	_	0	_	Vcc -1.5	V
Voltage Gain	Av	$R_{L} \geq 2k\Omega$	25	100	_	V/mV
Output Voltage	Vон	$Vcc = 30V, RL = 2k\Omega$	26	28	_	V
	Vol	$Vcc = 5V, R_{\perp} \le 10k\Omega$	_	5	20	mV
Output Current	Isource	Vcc = 15V, Vin + = 1V	20	40	_	mA
	Isink	Vcc = 15V, Vin - = 1V	10	20	_	mA
Common Mode Rejection Ratio	CMRR	_	65	85	_	dB
Power Supply Voltage Rejecton Ratio	SVRR	_	65	100	_	dB
Channel Separation	CS	_		120	_	dB

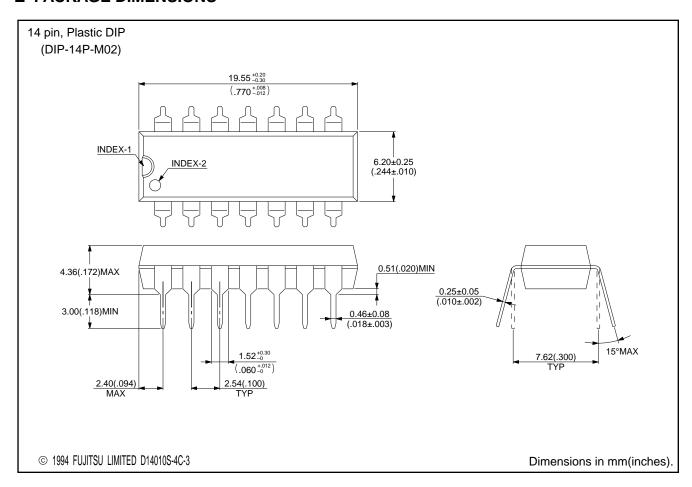
### Note:

<sup>\*</sup> A direction of the input bias current flows from IC because first input transistor consists of PNP.

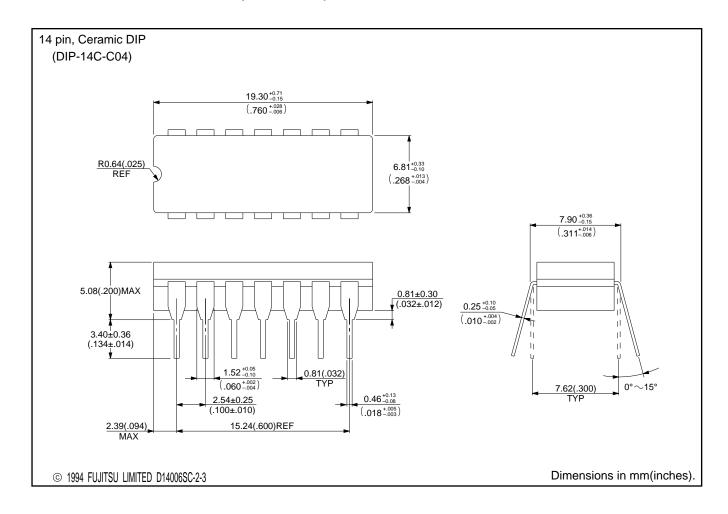
## **■ TYPICAL CHARACTERISTICS CURVES**



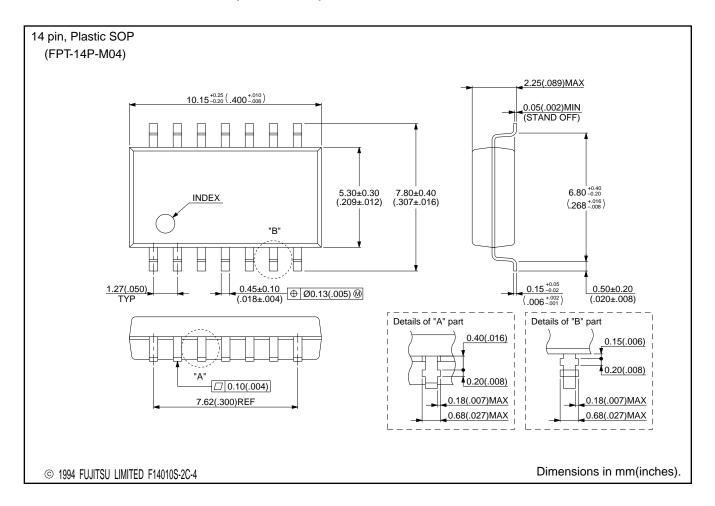
# **■ PACKAGE DIMENSIONS**



# **■ PACKAGE DIMENSIONS (Continued)**



# **■ PACKAGE DIMENSIONS (Continued)**



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