TN5320A — PNP High Voltage Amplifier



TN5320A PNP High Voltage Amplifier

- This device is designed for general purpose medium power amplifiers and switches requiring collector currents to 1.5 A.
- · Sourced from Process 34.



TO-226

1. Collector 2. Base 3. Emitter

Absolute Maximum Ratings* T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	75	V
V _{CEO}	Collector-Emitter Voltage	75	V
V _{EBO}	Emitter-Base Voltage	7	V
I _C	Collector Current - Continuous	500	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ +150	°C

^{*} This ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES

- 1) These rating are based on a maximum junction temperature of 150 degrees ${\sf C}.$
- 2) These are steady limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Max.	Units	
P_{D}	Total Device Dissipation	1.0	mW	
	Derate above 25°C	8.0	mW/°C	
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	50	°C/W	

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$\textbf{Electrical Characteristics*} \ \, \textbf{T}_{a} = 25 \text{xC unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Max.	Units

Off Characteristics

V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	$I_C = 100 \text{mA}, I_B = 0$	75		٧
		Vce = 100 V, VBE = 1.5 V (rev.)		100	μΑ
I _{CEX}	Collector Cut-off Current	Vce = 70 V, Vbe = 1.5 V (rev.) T = +150 °C		5	mA
I _{EBO}	Emitter Cut-off Current	V _{EB} = 7.0 V		100	μА

On Characteristics

h _{FE}	DC Current Gain	$V_{CE} = 4.0 \text{ V, } I_{C} = 0.5 \text{ mA},$ $V_{CE} = 2.0 \text{ V, } I_{C} = 1.0 \text{ mA},$	30 10	130	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	Ic = 500 mA, I _B = 50 mA		0.5	V
V _{BE} (on)	Base-Emitter Voltage	Vce = 4.0 V, Ic = 500 mA		1.1	V

^{*} Pulse Test: Pulse Width ≤ 300ms, Duty Cycle = 2%

NOTES

¹⁾ All voltages (V) and currents (A) are negative polarity for PNP transistors.





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