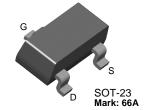


MMBF5103

N-Channel Switch

- This device is designed for low level analog switching, sample and hold circuits and chopper stabailzed amplifiers.
- Sourced from Process 51.
- See J111 for characteristics.



1. Drain 2. Source 3. Gate

Absolute Maximum Ratings* T_a=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	40	V
V _{GS}	Gate-Source Voltage	-40	V
I _{GF}	Forward Gate Current	50	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	- 55 ~ 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Charact	eristics	·			•
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = 1.0 \mu A, V_{DS} = 0$	-40		V
I _{GSS}	Gate Reverse Current	$V_{GS} = -15V, V_{DS} = 0$ $V_{GS} = -15V, V_{DS} = 0, T_a = 125^{\circ}C$		-200 -500	pA nA
V _{GS(off)}	Gate-Source Cutoff Voltage	$V_{DS} = 20V, I_{D} = 1.0nA$	-1.2	-2.7	V
V _{GS(f)}	Gate-Source Forward Voltage	$I_G = 1.0 \text{mA}, V_{DS} = 0$		1.0	V
On Characte	eristics	·			•
I _{DSS}	Zero-Gate Voltage Drain Current *	V _{DS} = 15V, V _{GS} = 0	10	40	mA
Small Signa	I Characteristics	·			•
C _{ISS}	Input Capacitance	$V_{DS} = 15V, V_{GS} = 0, f = 1.0MHz$		16	pF
C _{rss}	Reverse Transfer Capacitance	V _{GS} = -15V, f = 1.0MHz		6.0	pF

^{*} Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 1.0%

Thermal Characteristics T_a=25°C unless otherwise noted

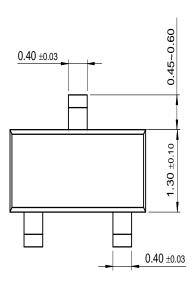
Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	556	°C/W

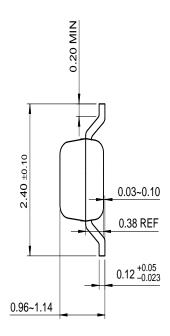
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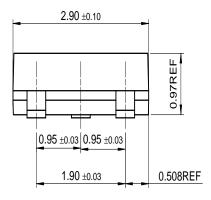
These ratings are based on a maximum junction temperature of 150 degrees C.
 These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

Package Dimensions

SOT-23







Dimensions in Millimeters

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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