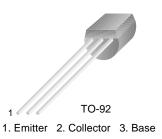
### FAIRCHILD

SEMICONDUCTOR®

### **KSA992**

### **Audio Frequency Low Noise Amplifier**

• Complement to KSC1845



# **PNP Epitaxial Silicon Transistor**

Absolute Maximum Ratings  $T_a=25^{\circ}C$  unless otherwise noted

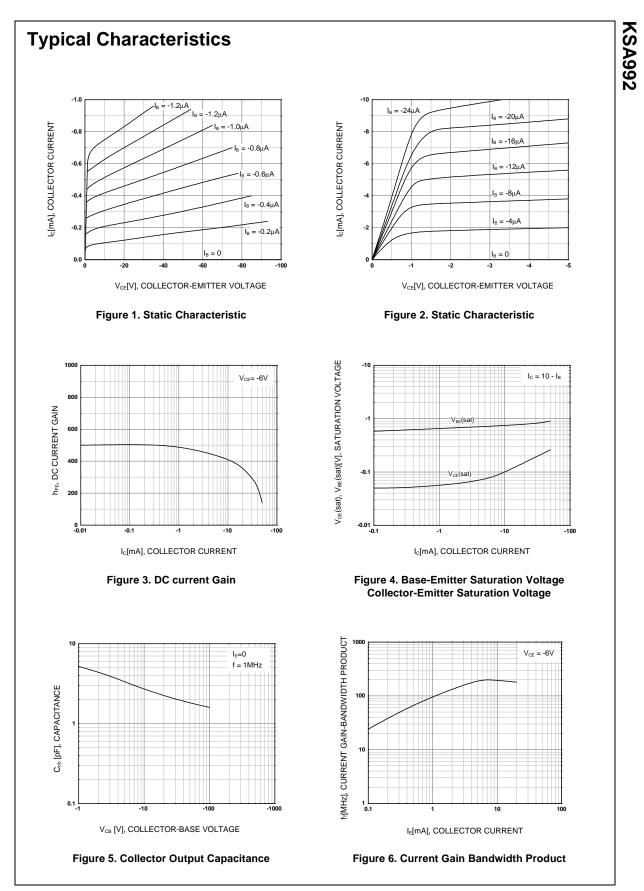
Symbol	Parameter	Ratings	Units V	
V <sub>CBO</sub>	Collector-Base Voltage	-120		
V <sub>CEO</sub>	Collector-Emitter Voltage	-120	V	
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V	
I <sub>C</sub>	Collector Current	-50	mA	
I <sub>B</sub>	Base Current	-10	mA	
P <sub>C</sub>	Collector Power Dissipation	500	mW	
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	-55 ~ 150	°C	

### **Electrical Characteristics** $T_a=25^{\circ}C$ unless otherwise noted

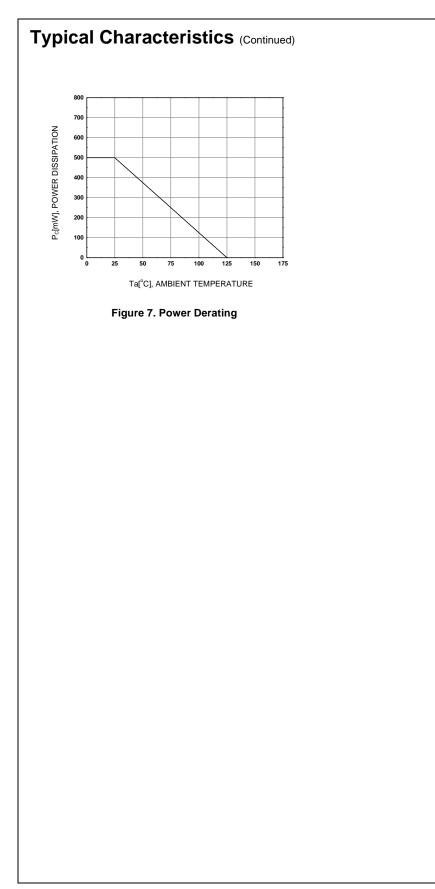
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = -120V, I <sub>E</sub> =0			-50	nA
I <sub>CEO</sub>	Collector Cur-off Current	V <sub>CE</sub> = -100V, I <sub>B</sub> =0			-1	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	V <sub>EB</sub> = -5mA, I <sub>C</sub> =0			-50	nA
h <sub>FE1</sub> h <sub>FE2</sub>	DC Current Gain	$V_{CE}$ = -6V, $I_{C}$ = -0.1mA $V_{CE}$ = -6V, $I_{C}$ = -1mA	150 200	500 500	800	
V <sub>BE</sub> (on)	Base-Emitter On Voltage	V <sub>CE</sub> = -6V, I <sub>C</sub> = -1mA	-0.55	-0.61	-0.65	V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1mA		-0.09	-0.3	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = -6V, I <sub>C</sub> = -1mA	50	100		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = -30V, I <sub>E</sub> =0, f=1MHz		2	3	pF
NV	Noise Voltage	$V_{CE}$ = -5.0V, I <sub>C</sub> = -1.0mA, R <sub>G</sub> =100KW, G <sub>V</sub> = 80dB, f = 10Hz to 1.0KHz		25	40	mV

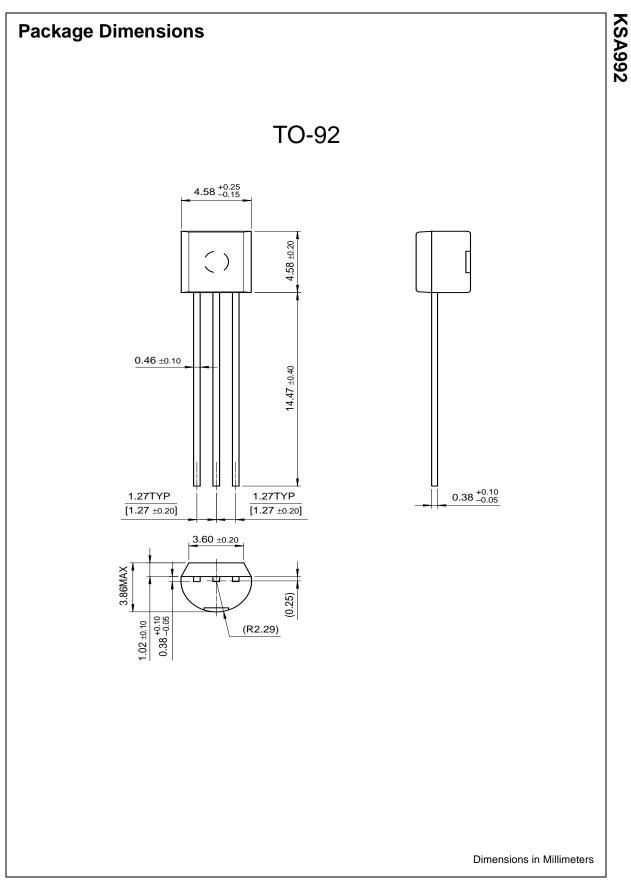
# h<sub>FE2</sub> Classification

Classification	Р	F	E
h <sub>FE2</sub>	200 ~ 400	300 ~ 600	400 ~ 800



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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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