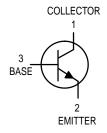
VHF Transistor

NPN Silicon



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCEO	20	Vdc
Collector-Base Voltage	V _{CBO}	30	Vdc
Emitter-Base Voltage	V _{EBO}	3.0	Vdc
Collector Current — Continuous	IC	100	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12	Watt mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

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THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	200	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	83.3	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				
Collector-Emitter Breakdown Voltage (I _C = 1.0 mAdc, I _B = 0)	V(BR)CEO	20	_	_	Vdc
Collector-Base Breakdown Voltage (I _C = 10 μAdc, I _E = 0)	V(BR)CBO	30	_	_	Vdc
Emitter-Base Breakdown Voltage (I _E = 10 μAdc, I _C = 0)	V(BR)EBO	3.0	_	_	Vdc
Collector Cutoff Current (V _{CB} = 20 Vdc, I _E = 0)	ICBO	_	_	100	nAdc
ON CHARACTERISTICS	•				
DC Current Gain (IC = 5.0 mAdc , VCE = 10 Vdc) (IC = 20 mAdc , VCE = 10 Vdc)	hFE	35 40	_ _	_ _	_
Collector-Emitter Saturation Voltage (I _C = 30 mAdc, I _B = 2.0 mAdc)	VCE(sat)	_	_	1.0	Vdc
Base-Emitter Saturation Voltage (I _C = 30 mAdc, I _B = 2.0 mAdc)	V _{BE(sat)}	_	_	1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS	•	•			
Current-Gain — Bandwidth Product (IC = 20 mAdc, V_{CE} = 10 Vdc, f = 100 MHz) (IC = 30 mAdc, V_{CE} = 10 Vdc, f = 100 MHz)	fΤ	700 600		_ _	MHz
Common Emitter Feedback Capacitance (V _{CB} = 10 Vdc, P _f = 0, f = 10 MHz)	C _{re}	_	0.65	_	pF
Noise Figure (I _C = 4.0 mA, V_{CE} = 10 V, R_{S} = 50 Ω , f = 200 MHz)	Nf	_	3.0	_	dB

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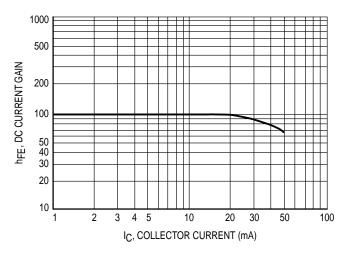


Figure 1. hFE at 10 V

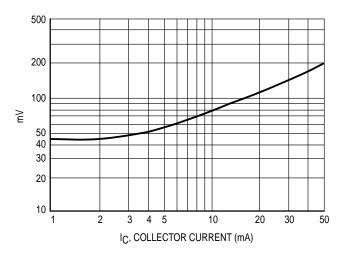


Figure 2. VCE(sat) at IC/IB = 10

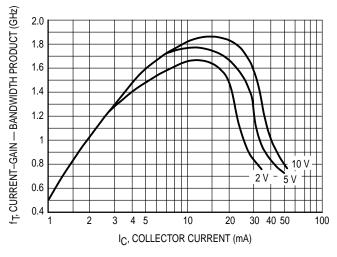


Figure 3. Current-Gain — Bandwidth Product

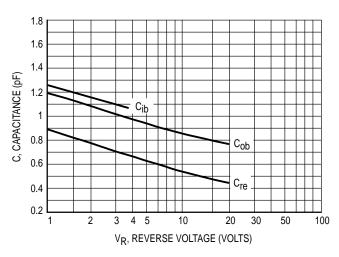


Figure 4. Capacitances

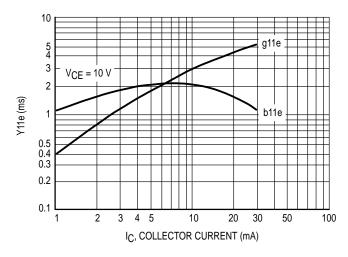


Figure 5. Input Impedance at 30 MHz

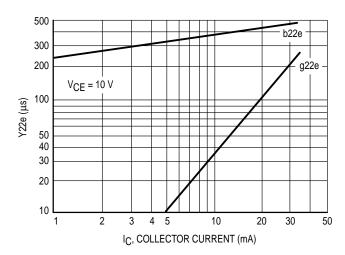
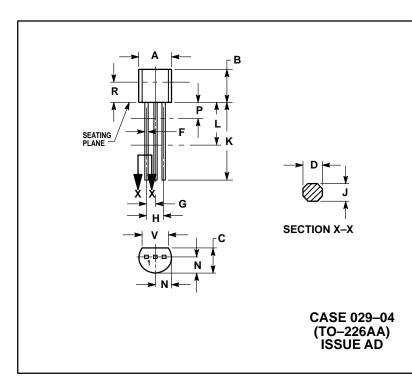


Figure 6. Output Impedance at 30 MHz

PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. CONTROLLING PERCHAGE BEYOND DIMENSION R IS UNCONTROLLED.
 4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION DAND J APPLY BETWEEN L AND K MINIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.45	5.20	
В	0.170	0.210	4.32	5.33	
C	0.125	0.165	3.18	4.19	
D	0.016	0.022	0.41	0.55	
F	0.016	0.019	0.41	0.48	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
7	0.015	0.020	0.39	0.50	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0.115		2.93		
٧	0.135		3 43		

STYLE 21:
PIN 1. COLLECTOR
2. EMITTER
3. BASE

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