

DATA SHEET

74F641

Octal bus transceiver with
common output enable,
non-inverting (open collector)

Product specification
Supersedes data of 1999 Jan 08
IC15 Data Handbook

2000 Jun 30

Octal bus transceiver with common output enable,
non-inverting (open-collector)

74F641

FEATURES

- High-impedance NPN base inputs for reduced loading (20µA in High and Low states)
- Octal bidirectional bus interface
- Common Output Enable for both Transmit and Receive modes
- Open collector outputs sink 64mA
- Non-inverting

ORDERING INFORMATION

DESCRIPTION	COMMERCIAL RANGE V _{CC} = 5V ±10%, T _{amb} = 0°C to +70°C	PKG DWG #
20-pin plastic DIP	N74F641N	SOT146-1
20-pin plastic SOL	N74F641D	SOT163-1

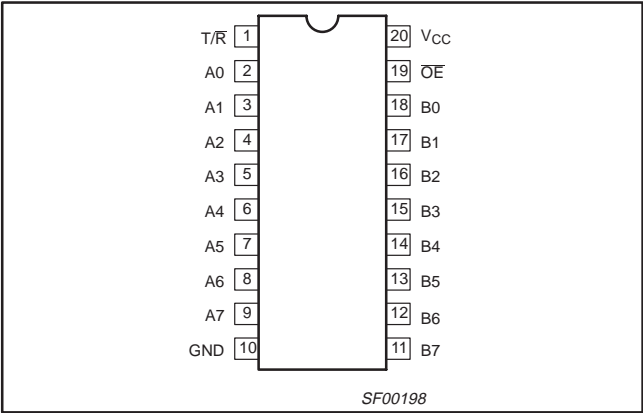
TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74F641	8.0ns	69mA

INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

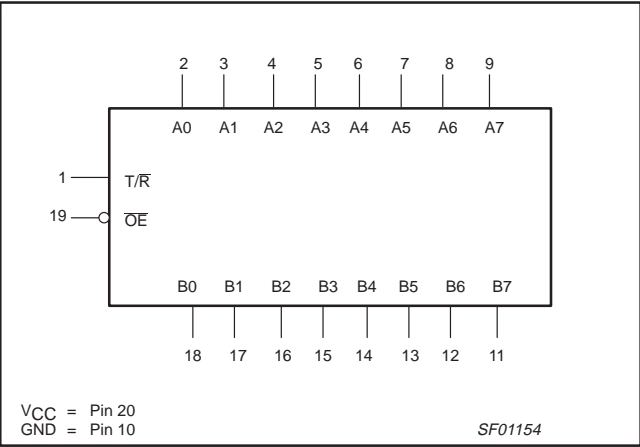
PINS	DESCRIPTION	74F(U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
A0 - A7, B0 - B7	Data inputs	1.0/0.033	20µA/20µA
T/R	Transmit/Receive input	2.0/0.067	40µA/40µA
OE	Output Enable inputs	2.0/0.067	40µA/40µA
A0 - A7	Data outputs	OC/40	OC/24mA
B0 - B7	Data outputs	OC/106.7	OC/64mA

NOTE: One (1.0) FAST unit load is defined as: 20µA in the High state and 0.6mA in the Low state. OC = Open Collector.

PIN CONFIGURATION



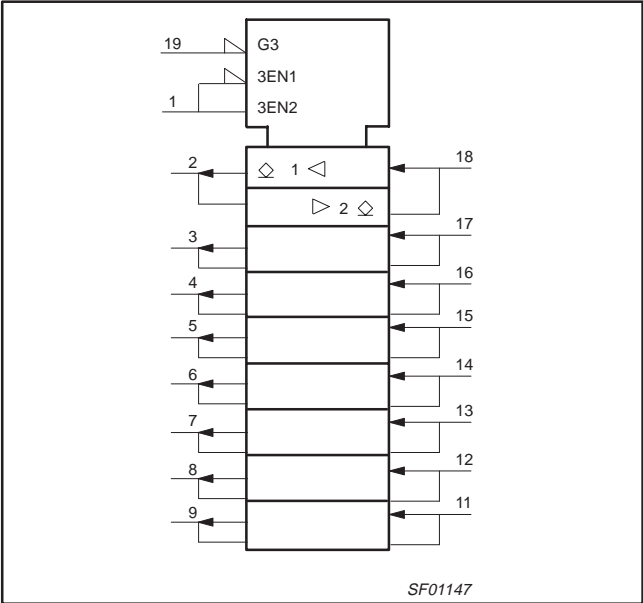
LOGIC SYMBOL



Octal bus transceiver with common output enable,
non-inverting (open-collector)

74F641

LOGIC SYMBOL (IEEE/IEC)

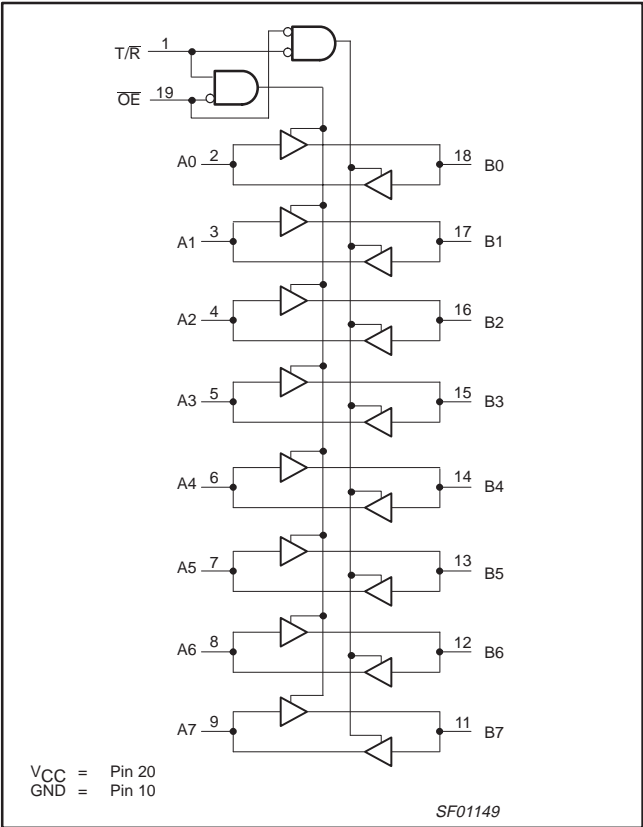


FUNCTION TABLE

INPUTS		INPUTS/OUTPUTS	
OE	T/R	An	Bn
L	L	A=B	INPUTS
H	H	INPUTS	B=A
H	X	OFF	OFF

H = High voltage level
L = Low voltage level
X = Don't care
OFF= High if pull-up resistor is connected to open collector output

LOGIC DIAGRAM



Octal bus transceiver with common output enable, non-inverting (open-collector)

74F641

ABSOLUTE MAXIMUM RATINGS

(Operation beyond the limits set forth in this table may impair the useful life of the device.
Unless otherwise noted these limits are over the operating free-air temperature range.)

SYMBOL	PARAMETER		RATING	UNIT
V_{CC}	Supply voltage		-0.5 to +7.0	V
V_{IN}	Input voltage		-0.5 to +7.0	V
I_{IN}	Input current		-30 to +5	mA
V_{OUT}	Voltage applied to output in High output state		-0.5 to + V_{CC}	V
I_{OUT}	Current applied to output in Low output state	A0–A7	48	mA
		B0–B7	128	mA
T_{amb}	Operating free-air temperature range		0 to +70	°C
T_{stg}	Storage temperature range		-65 to +150	°C

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER		LIMITS			UNIT
			MIN	NOM	MAX	
V_{CC}	Supply voltage		4.5	5.0	5.5	V
V_{IH}	High-level input voltage		2.0			V
V_{IL}	Low-level input voltage				0.8	V
I_{IK}	Input clamp current				-18	mA
I_{OH}	High-level output current				4.5	V
I_{OL}	Low-level output current	A0–A7			24	mA
		B0–B7			64	mA
T_{amb}	Operating free-air temperature range		0		70	°C

DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER		TEST CONDITIONS ¹			LIMITS			UNIT
						MIN	TYP ²	MAX	
I _{OH}	High-level output current		V _{CC} = MIN, V _{IL} = MAX, V _{IH} = MIN, V _{OH} = MAX					250	μA
V _{OL}	Low-level output voltage	A0–A7	V _{CC} = MIN, V _{IL} = MAX, V _{IH} = MIN,	I _{OL} = 24mA	±10%V _{CC}		0.35	0.50	V
					±5%V _{CC}		0.35	0.50	V
		B0–B7		I _{OL} = 48mA	±10%V _{CC}		0.38	0.55	V
				I _{OL} = 64mA	±5%V _{CC}		0.42	0.55	V
V _{IK}	Input clamp voltage		V _{CC} = MIN, I _I = I _{IK}				−0.73	−1.2	V
I _I	Input current at maximum input voltage	T/R, $\overline{\text{OE}}$	V _{CC} = 0.0V, V _I = 7.0V					100	μA
		An, Bn	V _{CC} = 5.5V, V _I = 5.5V					1	mA
I _{IH}	High-level input current	T/R, $\overline{\text{OE}}$	V _{CC} = MAX, V _I = 2.7V					40	μA
		An, Bn						20	μA
I _{IL}	Low-level input current	T/R, $\overline{\text{OE}}$	V _{CC} = MAX, V _I = 0.5V					−40	μA
		An, Bn						−20	μA
I _{CC}	Supply current (total)	74F641	I _{CCH}	V _{CC} = MAX	An=T/R=4.5, $\overline{\text{OE}}$ =GND		60	90	mA
		I _{CCL}			T/R=4.5V, An= $\overline{\text{OE}}$ =GND		78	120	mA

NOTES:

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- All typical values are at $V_{CC} = 5\text{V}$, $T_{amb} = 25^\circ\text{C}$.

Octal bus transceiver with common output enable,
non-inverting (open-collector)

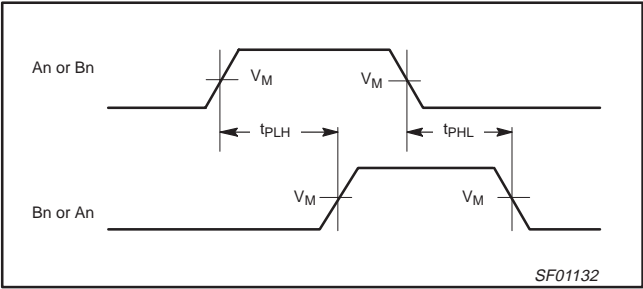
74F641

AC ELECTRICAL CHARACTERISTICS – 74F641

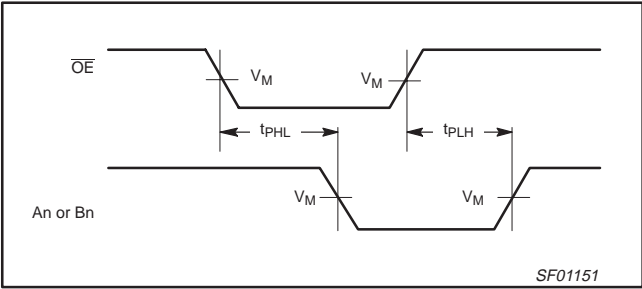
SYMBOL	PARAMETER	TEST CONDITION	LIMITS					UNIT
			V _{CC} = +5V T _{amb} = +25°C C _L = 50pF, R _L = 500Ω			V _{CC} = +5V ± 10% T _{amb} = 0°C to +70°C C _L = 50pF, R _L = 500Ω		
			MIN	TYP	MAX	MIN	MAX	
t _{PLH} t _{PHL}	Propagation delay An to Bn	Waveform 1	6.5 4.0	8.5 6.0	11.5 9.5	6.5 4.0	12.5 11.0	ns
t _{PLH} t _{PHL}	Propagation delay Bn to An	Waveform 1	6.0 3.5	8.0 5.5	11.5 7.5	6.0 3.5	12.0 8.0	ns
t _{PLH} t _{PHL}	Propagation delay OE to An	Waveform 2	7.0 5.0	10.5 7.0	12.5 9.0	7.0 5.0	13.0 10.0	ns
t _{PLH} t _{PHL}	Propagation delay OE to Bn	Waveform 2	8.0 3.5	9.0 7.5	12.5 9.5	8.0 5.5	13.5 10.5	ns

AC WAVEFORMS

For all waveforms, $V_M = 1.5V$.



Waveform 1. Propagation Delay for An to Bn or Bn to An



Waveform 2. Propagation Delay for \overline{OE} to An or Bn Outputs (Bn or An Inputs in Low State)

TEST CIRCUIT AND WAVEFORMS

Test Circuit for 3-State Outputs

TEST	SWITCH
t_{PLZ}	closed
t_{PZL}	closed
All other	open

DEFINITIONS:

R_L = Load resistor; see AC electrical characteristics for value.

C_L = Load capacitance includes jig and probe capacitance; see AC electrical characteristics for value.

R_T = Termination resistance should be equal to Z_{OUT} of pulse generators.

Input Pulse Definition

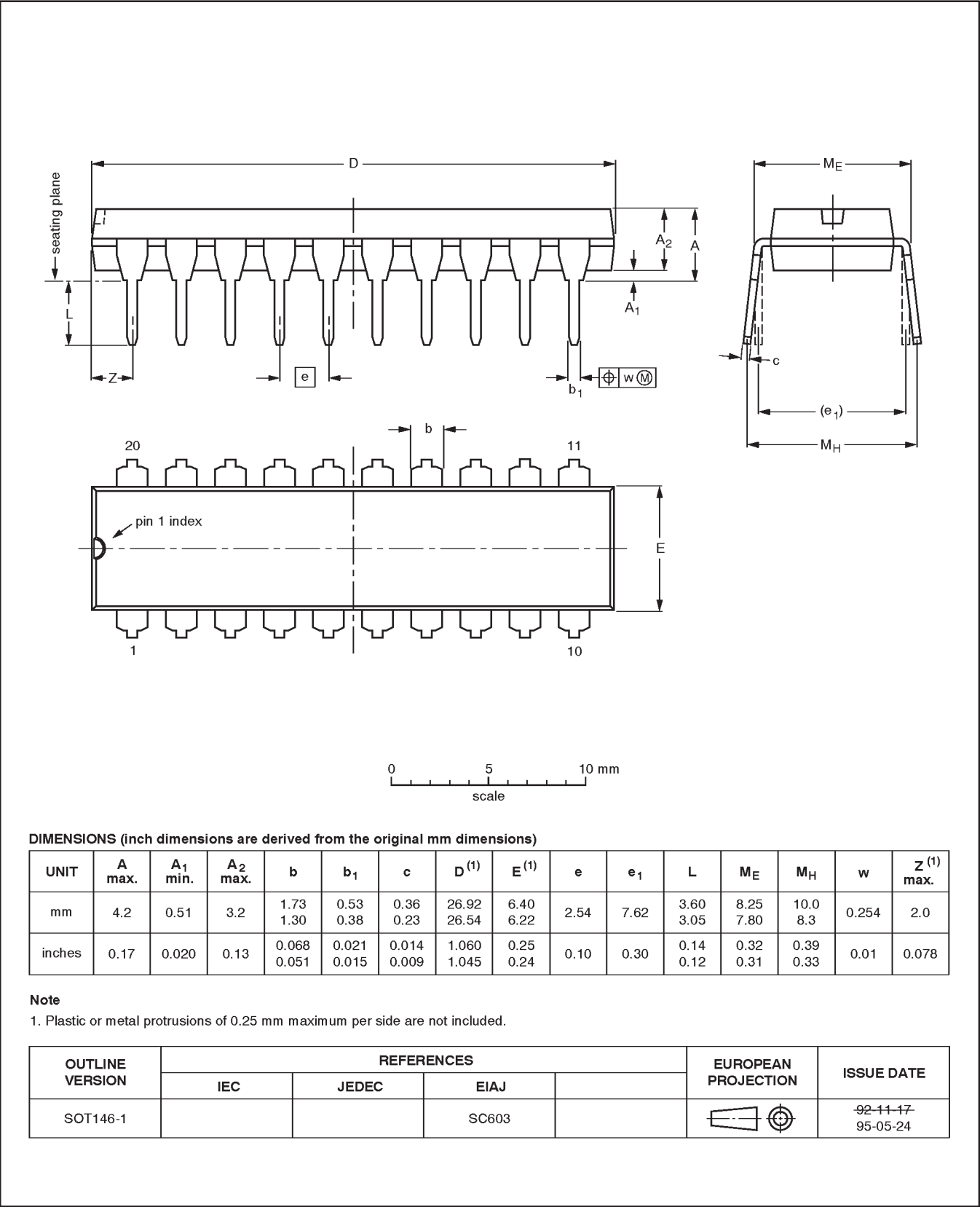
family	INPUT PULSE REQUIREMENTS					
	amplitude	V_M	rep. rate	t_w	t_{TLH}	t_{THL}
74F	3.0V	1.5V	1MHz	500ns	2.5ns	2.5ns

Octal bus transceiver with common output enable,
non-inverting (open collector)

74F641

DIP20: plastic dual in-line package; 20 leads (300 mil)

SOT146-1

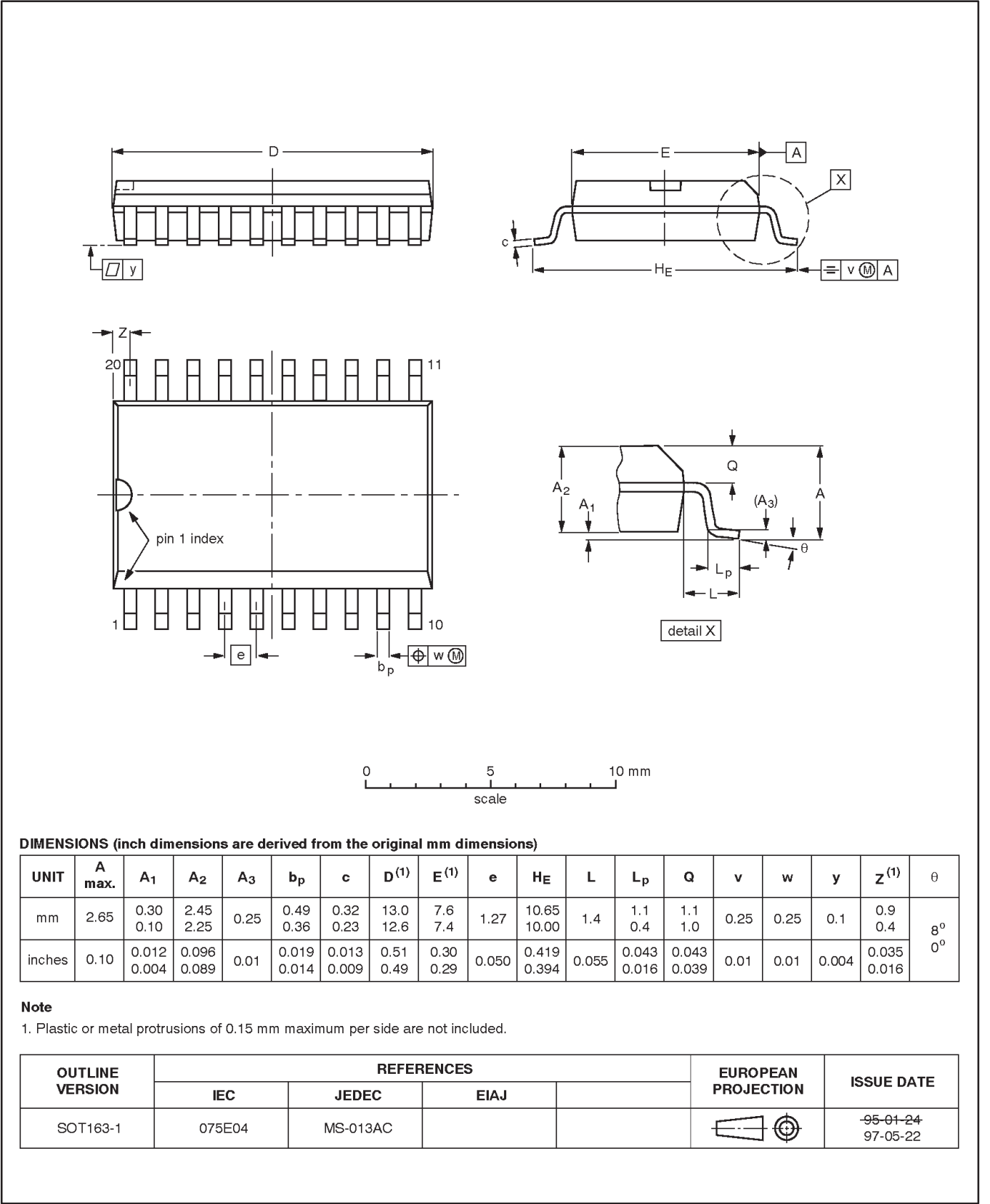


Octal bus transceiver with common output enable,
non-inverting (open collector)

74F641

SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1



Octal bus transceiver with common output enable, non-inverting (open collector)

74F641

Data sheet status

Data sheet status	Product status	Definition [1]
Objective specification	Development	This data sheet contains the design target or goal specifications for product development. Specification may change in any manner without notice.
Preliminary specification	Qualification	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
Product specification	Production	This data sheet contains final specifications. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.

[1] Please consult the most recently issued datasheet before initiating or completing a design.

Definitions

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information — Applications that are described herein for any of these products are for illustrative purposes only. Philips Semiconductors make no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Disclaimers

Life support — These products are not designed for use in life support appliances, devices or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips Semiconductors customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips Semiconductors for any damages resulting from such application.

Right to make changes — Philips Semiconductors reserves the right to make changes, without notice, in the products, including circuits, standard cells, and/or software, described or contained herein in order to improve design and/or performance. Philips Semiconductors assumes no responsibility or liability for the use of any of these products, conveys no license or title under any patent, copyright, or mask work right to these products, and makes no representations or warranties that these products are free from patent, copyright, or mask work right infringement, unless otherwise specified.

Philips Semiconductors
811 East Arques Avenue
P.O. Box 3409
Sunnyvale, California 94088-3409
Telephone 800-234-7381

© Copyright Philips Electronics North America Corporation 2000
All rights reserved. Printed in U.S.A.

Date of release: 06-00

Document order number:

9397-750 07277

Let's make things better.