## N-Channel Silicon MOSFET

## Features

- Low ON-resistance.
- Ultrahigh-speed switching.
- 4V drive.


## Specifications

Absolute Maximum Ratings at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings | Unit |
| :--- | :---: | :---: | :---: | :---: |
| Drain-to-Source Voltage | VDSS |  | 60 | V |
| Gate-to-Source Voltage | VGSS |  | $\pm 20$ | V |
| Drain Current (DC) | ID |  | 10 | A |
| Drain Current (Pulse) | IDP | PW $\leq 10 \mu \mathrm{~s}$, duty cycle $\leq 1 \%$ | 40 | A |
| Allowable Power Dissipation |  |  | 1 | W |
|  |  | Tc $=25^{\circ} \mathrm{C}$ | W |  |
| Channel Temperature | Tch |  | 10 | W |
| Storage Temperature | Tstg |  | ${ }^{\circ} \mathrm{C}$ |  |

Electrical Characteristics at $\mathrm{Ta}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Drain-to-Source Breakdown Voltage | V(BR)DSS | $\mathrm{I}=1 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | 60 |  |  | V |
| Zero-Gate Voltage Drain Current | IDSS | $\mathrm{V}_{\mathrm{DS}}=60 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  |  | 1 | $\mu \mathrm{A}$ |
| Gate-to-Source Leakage Current | IGSS | $\mathrm{V}_{\mathrm{GS}}= \pm 16 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ |  |  | $\pm 10$ | $\mu \mathrm{A}$ |
| Cutoff Voltage | $\mathrm{V}_{\mathrm{GS}}$ (off) | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{ID}=1 \mathrm{~mA}$ | 1.2 |  | 2.6 | V |
| Forward Transfer Admittance | \| yfs | | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{ID}=5 \mathrm{~A}$ | 4.5 | 7.5 |  | S |
| Static Drain-to-Source On-State Resistance | RDS(on)1 | $\mathrm{I}_{\mathrm{D}}=5 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}$ |  | 50 | 65 | $\mathrm{m} \Omega$ |
|  | RDS(on)2 | $\mathrm{I}=5 \mathrm{~A}, \mathrm{VGS}=4 \mathrm{~V}$ |  | 65 | 92 | $\mathrm{m} \Omega$ |
| Input Capacitance | Ciss | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 790 |  | pF |
| Output Capacitance | Coss | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 115 |  | pF |
| Reverse Transfer Capacitance | Crss | $\mathrm{V}_{\mathrm{DS}}=20 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |  | 88 |  | pF |
| Turn-ON Delay Time | $\mathrm{td}_{\mathrm{d}}(\mathrm{on})$ | See specified Test Circuit. |  | 10 |  | ns |
| Rise Time | $\mathrm{tr}_{r}$ | See specified Test Circuit. |  | 35 |  | ns |
| Turn-OFF Delay Time | td(off) | See specified Test Circuit. |  | 72 |  | ns |
| Fall Time | tf | See specified Test Circuit. |  | 55 |  | ns |

Marking : K3912
Continued on next page.

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| Parameter | Symbol | Conditions | Ratings |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | min | typ | max |  |
| Total Gate Charge | Qg | $\mathrm{V}_{\mathrm{DS}}=30 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I} \mathrm{D}=10 \mathrm{~A}$ |  | 16 |  | nC |
| Gate-to-Source Charge | Qgs | $\mathrm{V}_{\mathrm{DS}}=30 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{ID}=10 \mathrm{~A}$ |  | 4 |  | nC |
| Gate-to-Drain "Miller" Charge | Qgd | VDS $=30 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{ID}=10 \mathrm{~A}$ |  | 3.4 |  | nC |
| Diode Forward Voltage | $\mathrm{V}_{\text {SD }}$ | IS $=10 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  | 1.0 | 1.2 | V |

## Package Dimensions

unit : mm
7516-004


Switching Time Test Circuit

7516-004


1 : Source

: Drain

SANYO : TO-126ML


ID - VGS





Note on usage : Since the 2SK3912 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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