Ordering number : EN8635

SANYO Semiconductors
DATA SHEET

N-Channel Silicon MOSFET

2SK3745LS — High-Voltage, High-Speed Switching Applications

Features

- Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- High reliability (Adoption of HVP process).
- Micaless package facilitating mounting.
- Avalanche resistance guarantee.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		1500	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D*		2	A
Drain Current (Pulse)	I_{DP}		4	A
Allowable Power Dissipation	P_D		2.0	W
		Tc=25°C	35	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C
Avalanche Enargy (Single Pulse) *1	E_{AS}		42	mJ
Avalanche Current *2	I_{AV}		2	A

*Shows chip capability

*1 V_{DD}=99V, L=20mH, I_{AV}=2A

*2 L≤20mH, single pulse

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0V	1500			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=1200V, V_{GS}=0V			100	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}= ±16V, V_{DS}=0V			±10	μA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=10V, I_D=1mA	2.5		3.5	V
Forward Transfer Admittance	|yfs|	V_{DS}=20V, I_D=1A	0.7	1.4		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)}$	I_D=1A, V_{GS}=10V		10	13	Ω

Marking : K3745

Continued on next page.

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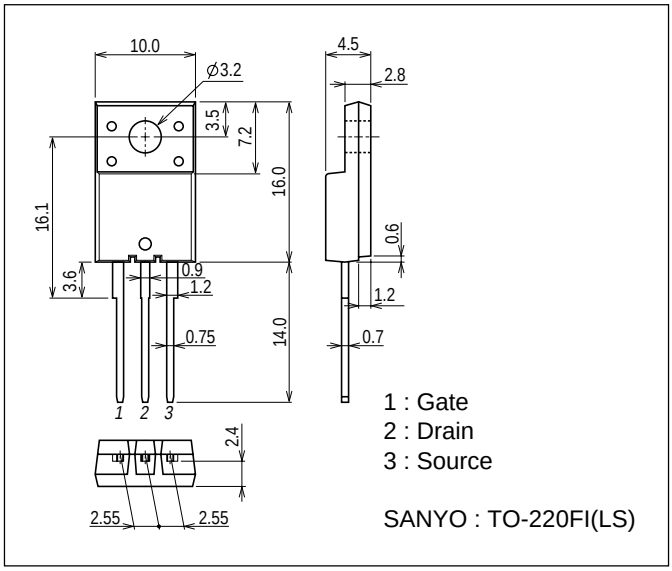
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=30V, f=1MHz		380		pF
Output Capacitance	Coss	V_{DS}=30V, f=1MHz		70		pF
Reverse Transfer Capacitance	Crss	V_{DS}=30V, f=1MHz		40		pF
Turn-ON Delay Time	t_{d}(on)	See specified Test Circuit.		12		ns
Rise Time	t_{r}	See specified Test Circuit.		37		ns
Turn-OFF Delay Time	t_{d}(off)	See specified Test Circuit.		152		ns
Fall Time	t_{f}	See specified Test Circuit.		59		ns
Total Gate Charge	Qg	V_{DS}=200V, V_{GS}=10V, I_{D}=2A		37.5		nC
Gate-to-Source Charge	Qgs	V_{DS}=200V, V_{GS}=10V, I_{D}=2A		2.7		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=200V, V_{GS}=10V, I_{D}=2A		20		nC
Diode Forward Voltage	V_{SD}	I_{S}=2A, V_{GS}=0V		0.88	1.2	V

Note) Although the protection diode is contained between gate and source, be careful of handling enough.

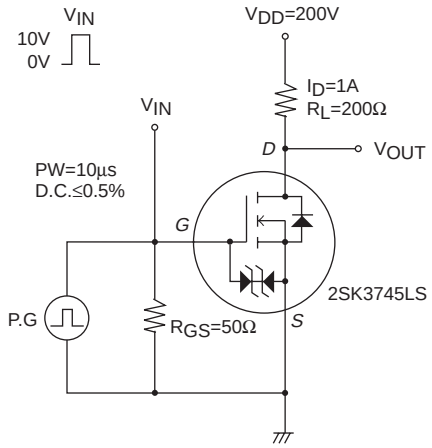
Package Dimensions

unit : mm

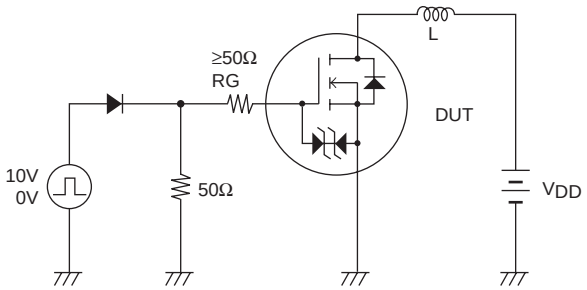
7509-002

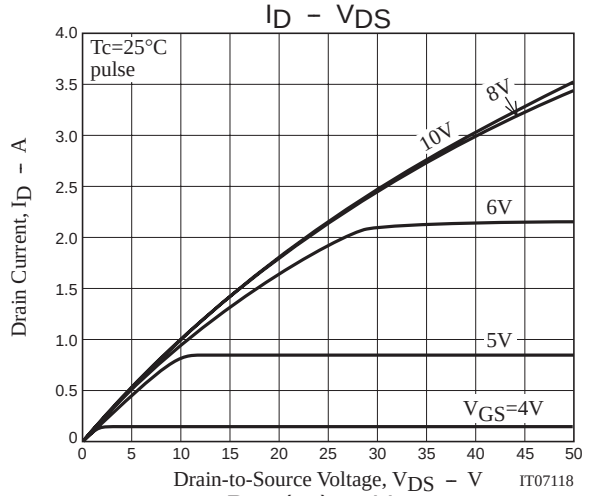


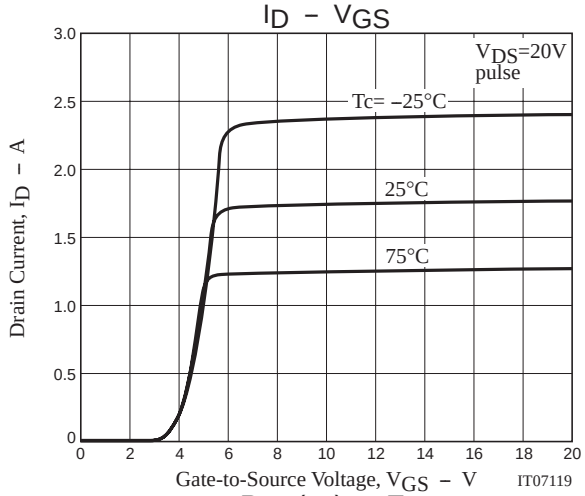
Switching Time Test Circuit

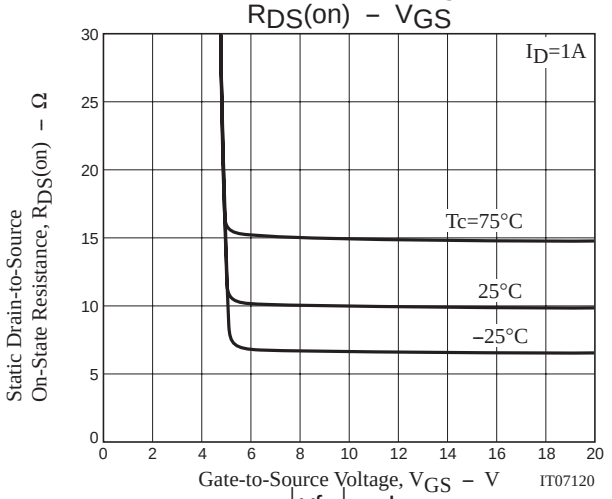


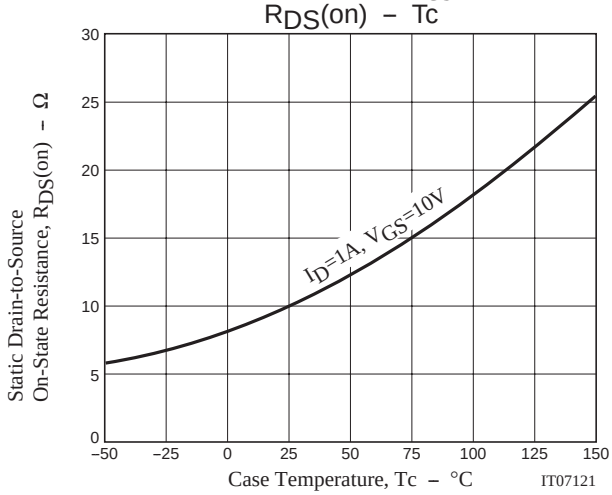
Avalanche Resistance Test Circuit

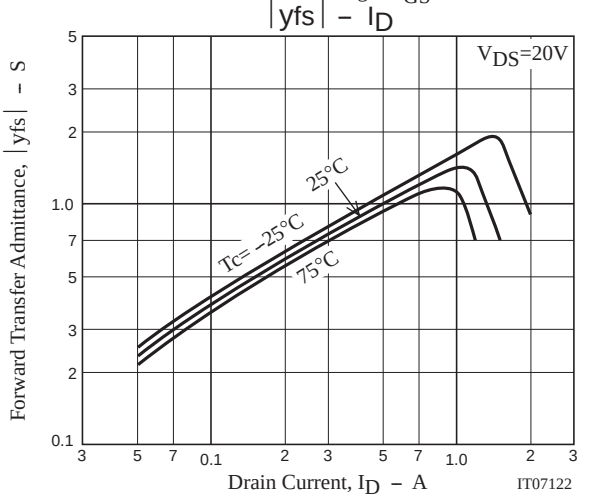


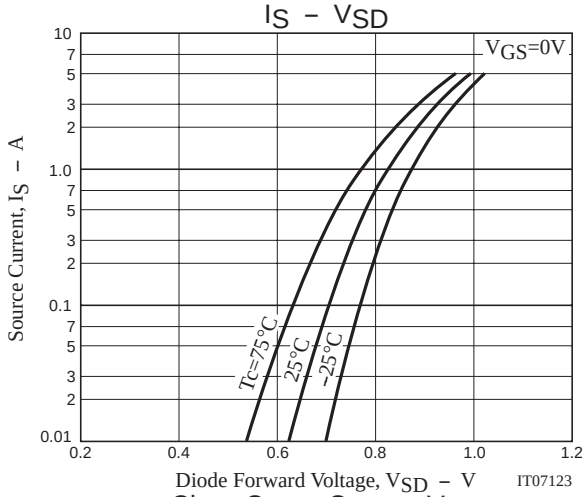

ID – VDS
Tc=25°C
pulse
8V
10V
6V
5V
VGS=4V
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT07118

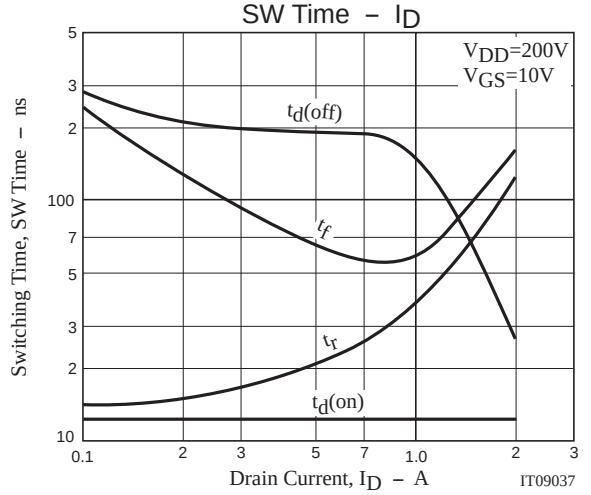

ID – VGS
VDS=20V
pulse
Tc= –25°C
25°C
75°C
Drain Current, ID – A
Gate-to-Source Voltage, VGS – V
IT07119

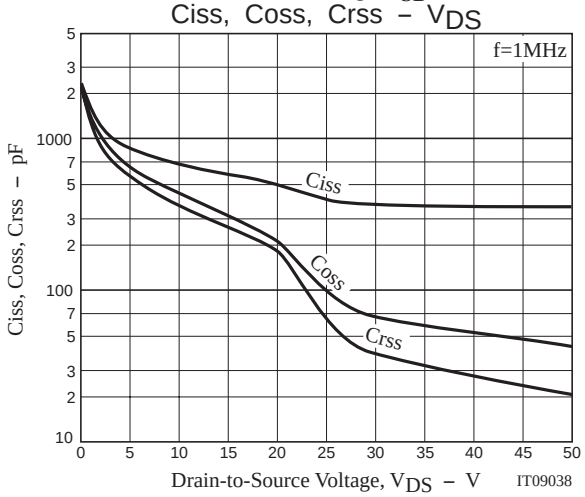

RDS(on) – VGS
ID=1A
Tc=75°C
25°C
–25°C
Static Drain-to-Source
On-State Resistance, RDS(on) – Ω
Gate-to-Source Voltage, VGS – V
IT07120


RDS(on) – Tc
ID=1A, VGS=10V
Static Drain-to-Source
On-State Resistance, RDS(on) – Ω
Case Temperature, Tc – °C
IT07121


|yfs| – ID
VDS=20V
25°C
Tc= –25°C
75°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A
IT07122


IS – VSD
VGS=0V
Tc=75°C
25°C
–25°C
Source Current, IS – A
Diode Forward Voltage, VSD – V
IT07123


SW Time – ID
VDD=200V
VGS=10V
td(off)
tf
tr
td(on)
Switching Time, SW Time – ns
Drain Current, ID – A
IT09037


Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT09038