



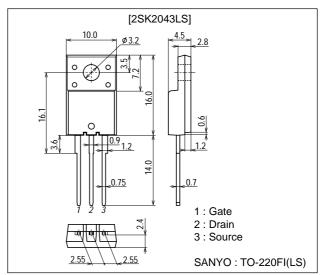
# **Ultrahigh-Speed Switching Applications**

### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- High-speed diode (t<sub>rr</sub>=100ns).
- · Micaless package facilitating mounting.

### **Package Dimensions**

unit : mm 2078C



# **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		600	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID		2	Α
Drain Current (Pulse)	IDP		8	Α
Allowable Power Dissipation	Pp		2.0	W
	PD	Tc=25°C	25	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0	600			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =480V, V <sub>GS</sub> =0			1.0	mA
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0			±100	nA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	2.0		3.0	V

(Note) Be careful in handling the 2SK2043LS because it has no protection diode between gate and source.

Continued on next page.

Marking: K2043

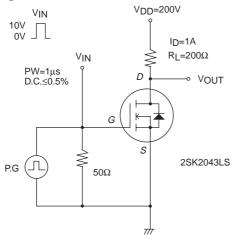
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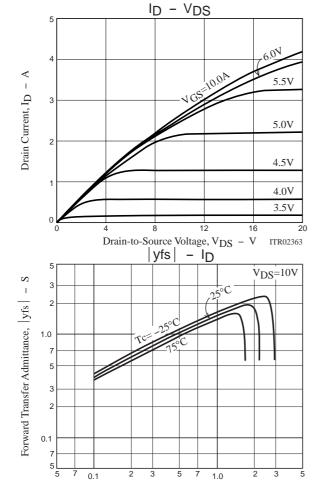
# 2SK2043LS

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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =1A	0.8	1.5		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =1A, V <sub>GS</sub> =10V		3.2	4.3	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz		400		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		55		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz		15		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		10		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		12		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		65		ns
Fall Time	tf	See specified Test Circuit.		40		ns
Diode Forward Voltage	V <sub>SD</sub>	IS=2A, VGS=0			1.5	V
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>S</sub> =2A, di/dt=100A/μs		100		ns

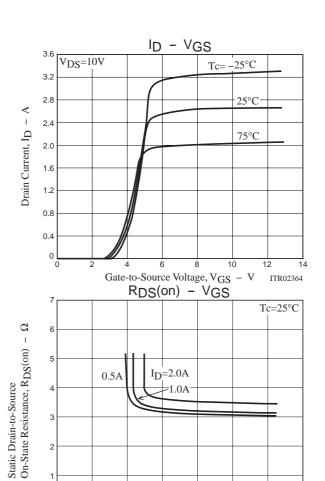
# **Switching Time Test Circuit**



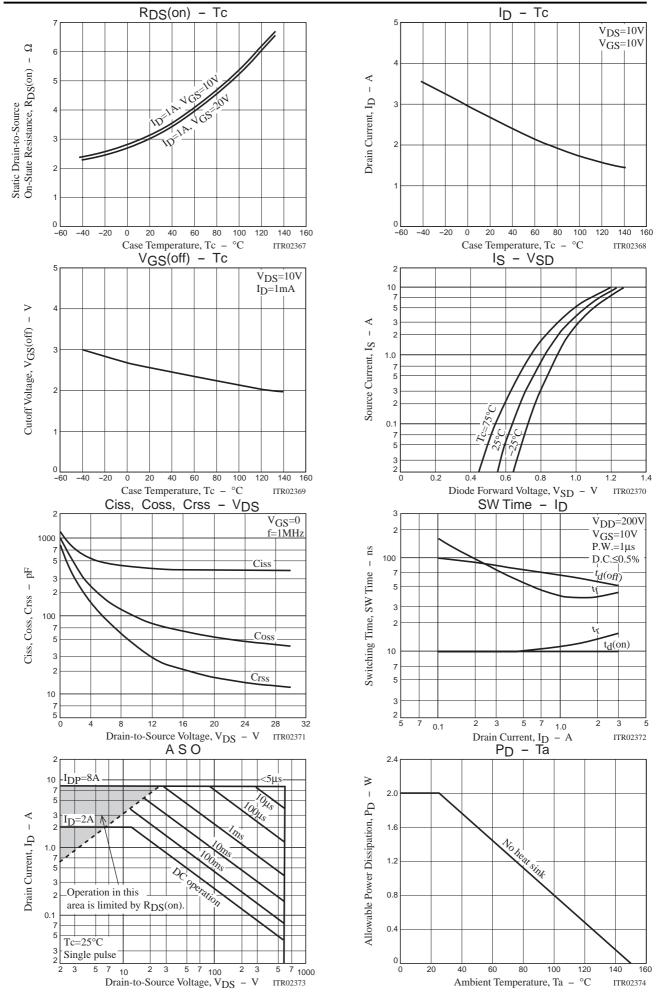


Drain Current, I<sub>D</sub> - A

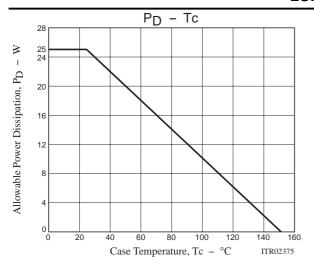
ITR02365



Gate-to-Source Voltage,  $V_{GS} - V$ 



### 2SK2043LS



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