Ordering number : ENA0578

SANYO Semiconductors

DATA SHEET

NPN Triple Diffused Planar Silicon Transistor

2SC6118LS — Color TV Horizontal Deflection Output Applications

Features

- High speed.
- High breakdown voltage (V_{CBO}=1500V).
- High reliability (Adoption of HVP process).
- Adoption of MBIT process.
- On-chip damper diode.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V_{CBO}		1500	V
Collector-to-Emitter Voltage	V_{CEO}		800	V
Emitter-to-Base Voltage	V_{EBO}		6	V
Collector Current	I_C		8	A
Collector Current (Pulse)	I_{CP}		20	A
Collector Dissipation	P_C		2.0	W
		Tc=25°C	35	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I_{CBO}	V_{CB}=800V, I_E=0A			10	μA
Collector Cutoff Current	I_{CES}	V_{CE}=1500V, R_{BE}=0Ω			1.0	mA
Collector Sustain Voltage	V_{CEO}(sus)	I_C=100mA, I_B=0A	800			V
Emitter Cutoff Current	I_{EBO}	V_{EB}=4V, I_C=0A	40		130	mA
Collector-to-Emitter Saturation Voltage	V_{CE}(sat)	I_C=4.5A, I_B=0.9A			2	V
Base-to-Emitter Saturation Voltage	V_{BE}(sat)	I_C=4.5A, I_B=0.9A			1.5	V
DC Current Gain	$h_{FE}1$	V_{CE}=5V, I_C=1A	10			
	$h_{FE}2$	V_{CE}=5V, I_C=5A	5.3		7.5	
Diode Forward Voltage	V_F	I_{EC}=7A			2	V
Fall Time	tf	I_C=3A, I_{B1}=0.6A, I_{B2}=-1.2A			0.2	μs

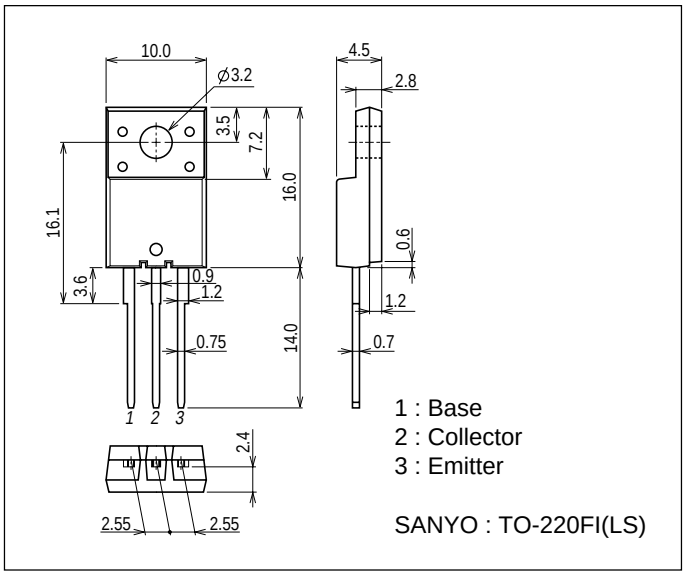
SANYO Semiconductor Co., Ltd.

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

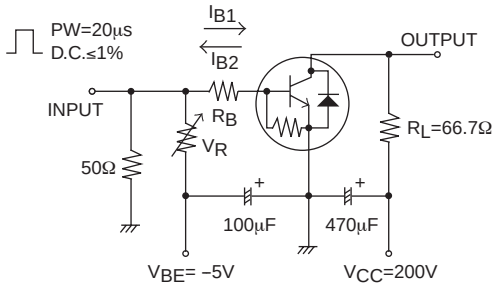
Package Dimensions

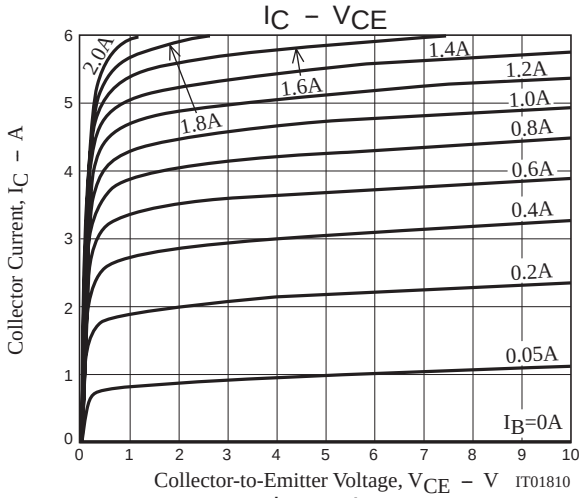
unit : mm (typ)

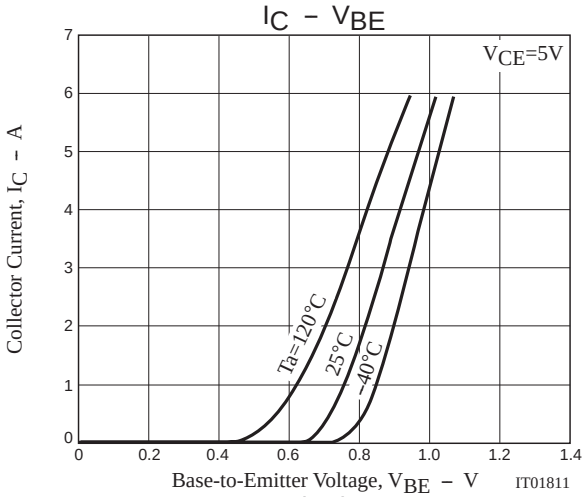
7509-003

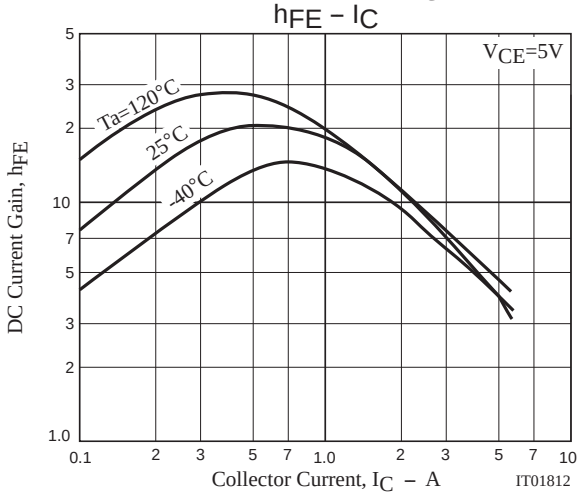


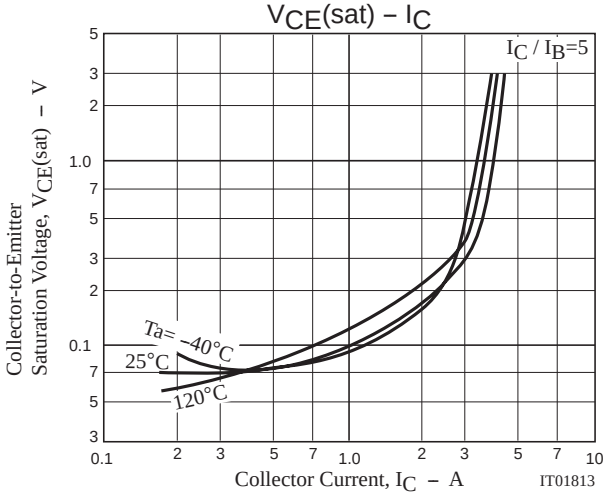
Switching Time Test Circuit



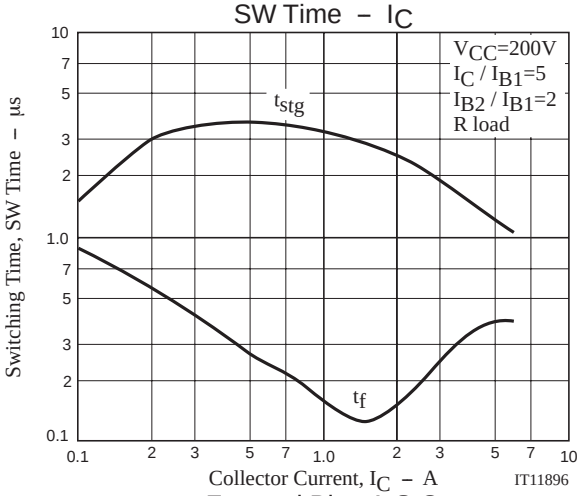




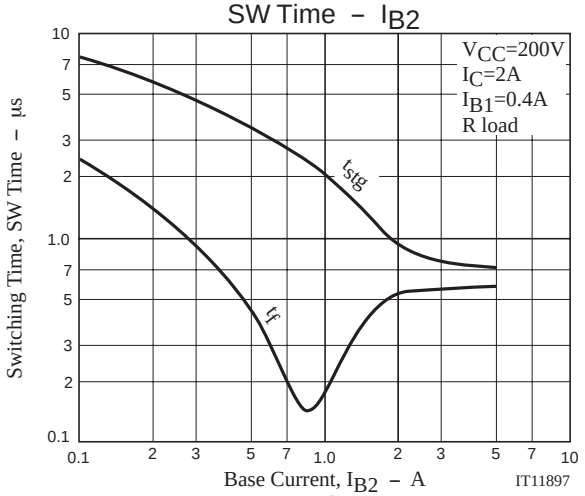




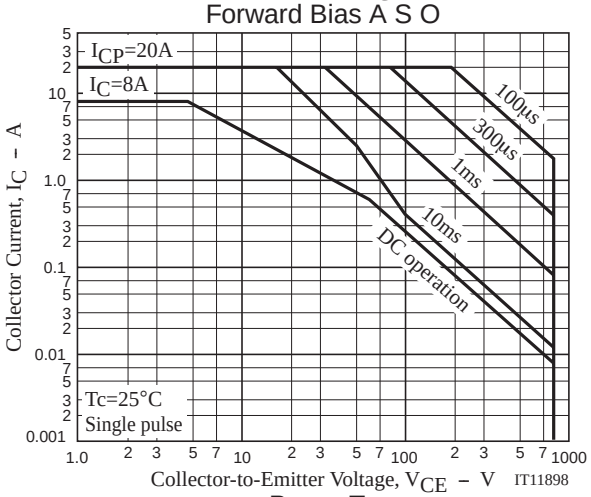
SW Time – I_C



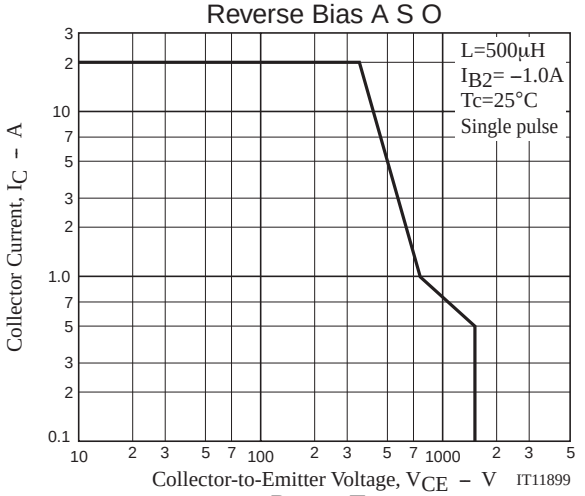
SW Time – I_{B2}



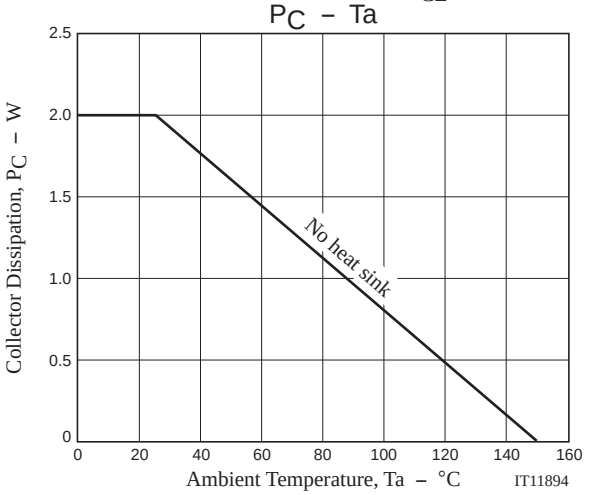
Forward Bias A S O



Reverse Bias A S O



P_C – Ta



P_C – Tc