

SANYO Semiconductors DATA SHEET

2SC5994 — NPN Epitaxial Planar Silicon Transistor

High-Current Switching Applications

Applications

· Voltage regulators, relay drivers, lamp drivers, electrical equipment.

Features

- · Adoption of MBIT process.
- · High current capacitance.
- · Low collector-to-emitter saturation voltage.
- · High-speed switching.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		100	V
Collector-to-Emitter Voltage	VCES		100	V
Collector-to-Emitter Voltage	VCEO		50	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		2	Α
Collector Current (Pulse)	ICP		4	Α
Base Current	lΒ		400	mA
Collector Dissipation	Do	Mounted on a ceramic board (450mm ² X0.8m)	1.3	W
	PC	Tc=25°C	3.5	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Unit
Collector Cutoff Current	ІСВО	VCB=50V, IE=0			1	μΑ
Emitter Cutoff Current	IEBO	V _{EB} =4V, I _C =0			1	μΑ
DC Current Gain	hFE1	VCE=2V, IC=100mA	200		560	
	hFE2	V _{CE} =2V, I _C =1.5A	40			

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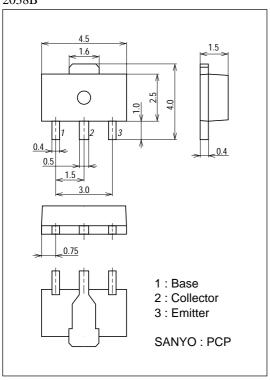
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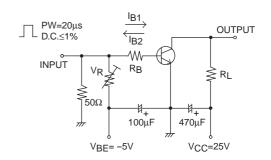
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Gain-Bandwidth Product	fT	VCE=10V, IC=300mA		420		MHz
Output Capacitance	Cob	V _{CB} =10V, f=1MHz		9		pF
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=1A, IB=50mA		135	300	mV
Base-to-Emitter Saturation Voltage	VBE(sat)	IC=1A, IB=50mA		0.9	1.2	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =10μA, I _E =0	100			V
Collector-to-Emitter Breakdown Voltage	V(BR)CES	IC=100μA, RBE=0	100			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	IC=1mA, RBE=∞	50			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =10μA, I _C =0	6			V
Turn-ON Time	ton	See specified Test Circuit.		30		ns
Storage Time	tstg	See specified Test Circuit.		330		ns
Fall Time	tf	See specified Test Circuit.		40		ns

Package Dimensions

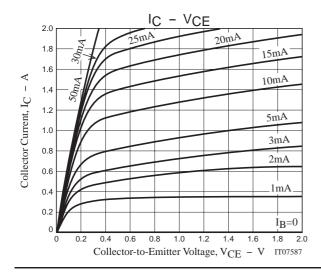
unit : mm 2038B

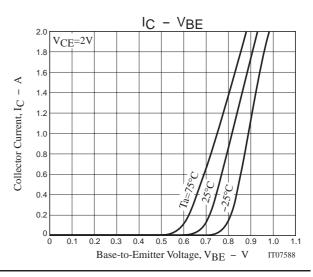


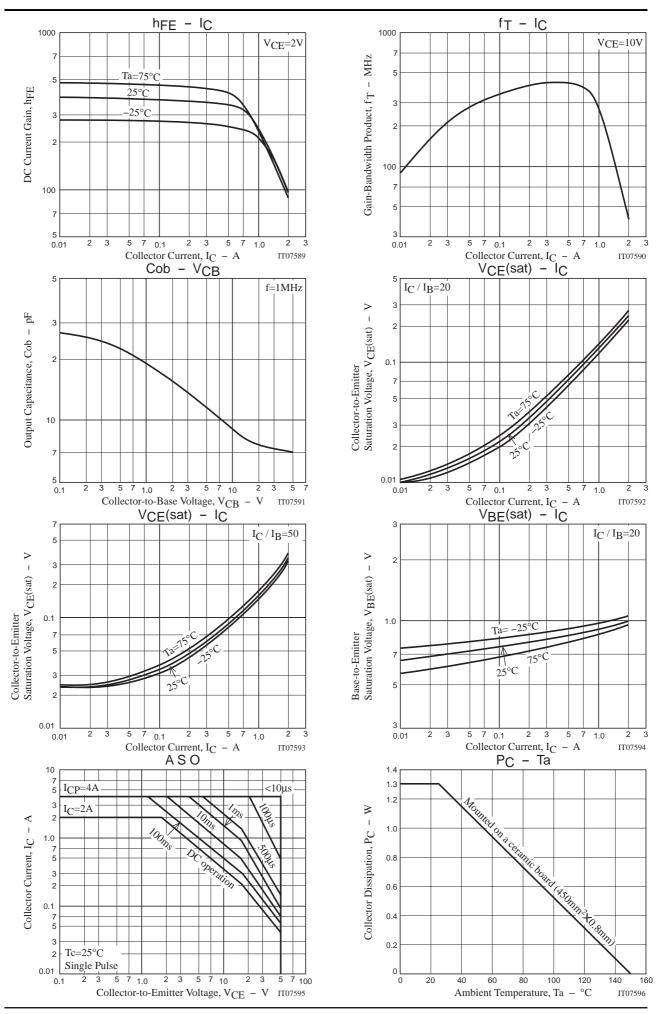
Switching Time Test Circuit

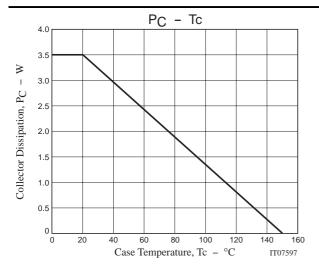


$$I_{C}=10I_{B1}=-10I_{B2}=700mA$$









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