



# **Driver Applications**

### **Applications**

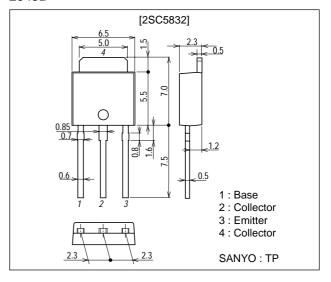
 Suitable for use in switching of inductive load (motor drivers, printer hammer drivers, relay drivers).

### **Features**

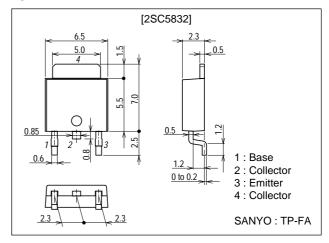
- · High DC current gain.
- · Wide ASO.
- On-chip zener diode of 65±10V between collector and base.
- · Uniformity in collector-to-base voltage.
- · Large inductive load handling capability.

## **Package Dimensions**

unit : mm 2045B



unit : mm 2044B



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# **Specifications**

# Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO	On-chip zener diode(65±10V)	55	V
Collector-to-Emitter Voltage	VCEO	On-chip zener diode(65±10V)	55	V
Emitter-to-Base Voltage	VEBO		6	V
Collector Current	IC		2	Α
Collector Current (Pulse)	ICP		4	Α
Collector Dissipation	Do		1.0	W
	PC	Tc=25°C	10	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

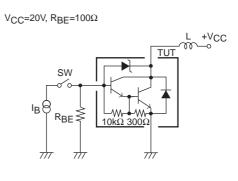
### Electrical Characteristics at Ta=25°C

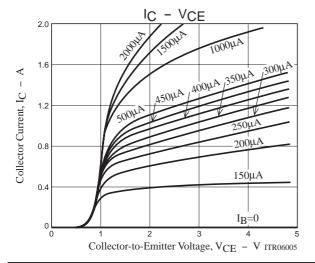
Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V <sub>CB</sub> =40V, I <sub>E</sub> =0			10	μΑ
Emitter Cutoff Current	IEBO	V <sub>EB</sub> =5V, I <sub>C</sub> =0			2	mA
DC Current Gain	hFE	V <sub>CE</sub> =5V, I <sub>C</sub> =1A	1000	4000		
Gain-Bandwidth Product	fT	V <sub>CE</sub> =5V, I <sub>C</sub> =1A		180		MHz
Inductive Load	Es/b	L=100mH, R <sub>BE</sub> =100Ω	25			mJ
Collector-to-Emitter Saturation Voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> =1A, I <sub>B</sub> =4mA		1.0	1.5	V
Base-to-Emitter Saturation Voltage	V <sub>BE</sub> (sat)	I <sub>C</sub> =1A, I <sub>B</sub> =4mA			2.0	V
Collector-to-Base Breakdown Voltage	V(BR)CBO	I <sub>C</sub> =100μA, I <sub>E</sub> =0	55	65	75	V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I <sub>C</sub> =1mA, R <sub>BE</sub> =∞	55	65	75	V
Turn-ON Time	ton	See specified Test Circuit.		0.2		μs
Storage Time	t <sub>stg</sub>	See specified Test Circuit.		3.5		μs
Fall Time	tf	See specified Test Circuit.		0.5		μs

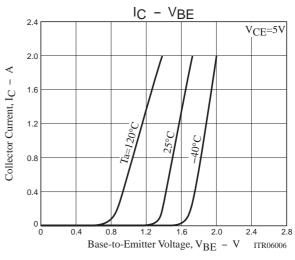
### **Switching Time Test Circuit**

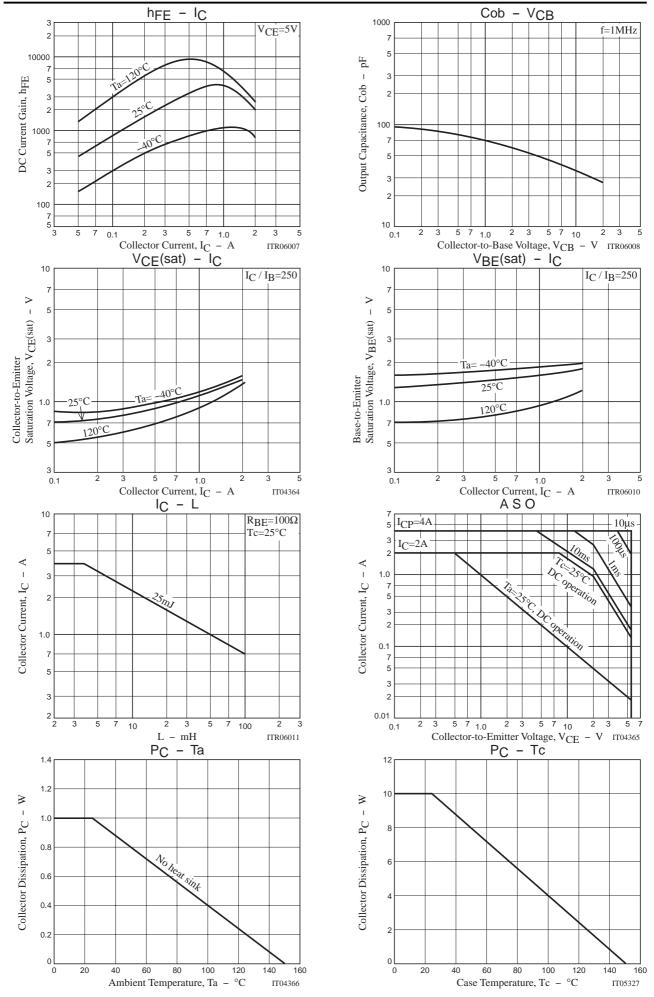
# PW=50 $\mu$ s, Duty Cycle≤1% OUTPUT $I_{B1}=-I_{B2}=4mA$ INPUT $R_{B}$ $V_{R}$ V

### Es / b Test Circuit









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